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Signature

Angie C. Farr

PTO/SB/21 (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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	Application No.	09/902,907	6
TRANSMITTAL FORM	Filing Date	July 10, 2001	20
e used for all correspondence after initial filing)	First Named Inventor	Brian D. Possley	The second
	Group Art Unit	2814	T ₀

Examiner Name

Total Number of Pages in This Submission 42390P6643C2 Attorney Docket Number **ENCLOSURES** (check all that apply) After Allowance Communication Assignment Papers (for an Application) Fee Transmittal Form Appeal Communication to Board of Appeals and Interferences Fee Attached Drawing(s) Appeal Communication to Group Amendment / Response Licensing-related Papers (Appeal Notice, Brief, Reply Brief) After Final **Proprietary Information** Petition Affidavits/declaration(s) Petition to Convert a Provisional Application Status Letter **Extension of Time Request** Other Enclosure(s) Power of Attorney, Revocation Change of Correspondence Address (please identify below): **Express Abandonment Request** Terminal Disclaimer -Return Receipt Postcard Information Disclosure Statement Certified Copy of Priority Document(s) Request for Refund Response to Missing Parts/ Incomplete Application CD, Number of CD(s) Remarks Response to Missing Parts under 37 CFR 1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Gregory D. Caldwell, Reg. No. 39,926 Individual name BLAKELY, SOKQLOFF, ZAYLOR & ZAFMAN LLP Signature October 17, 2002 Date **CERTIFICATE OF MAILING/TRANSMISSION** I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class mail with sufficient postage in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on: October 17, 2002

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Date

October 17, 2002

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Registration No. (503) 684-6200 Telephone Name (Print/Type) Gregory D. Caldwell 39,926 Attorney/Agent) 10/17/02 Signature Date

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	Attorney Docket: 042390.P6643c2							
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In re Patent Application of:	PATENT AND TRADEMARK OFFICERECE/VED OCT 30 2002 11-7-02 TECHNOLOGY CENTER 2800							
Possley) Examiner: Farahani, Dana							
Application No.: 09/902,907) Art Unit: 2814							
Filed: July 10, 2001								
For: Gate Array Architecture								

RESPONSE

ASSISTANT COMMISSIONER FOR PATENTS Washington, D.C. 20231

Dear Sir:

In response to the Office Action, dated July 17, 2002, please consider the following remarks.

REMARKS

The above-referenced patent application has been reviewed in light of the Office Action, dated July 17, 2002, in which: claims 13-15, 18 and 19 are rejected under 35 USC 102(b) as being unpatentable over Tran et al. (hereinafter "Tran"); claims 16 and 17 are rejected under 35 USC 103 as being unpatentable over Tran; and claim 20 is reject as being unpatentable under 35 USC 103 as being unpatentable over Tran in view of Farwell. Reconsideration of the above-referenced patent application in view of the following remarks is respectfully requested.

Claims 13-20 are pending. No claims have been cancelled, amended, or added. It is noted that the other claims in the application as originally filed are being pursued in separate related applications. The Examiner has rejected claims 13-15, 18 and 19 under 35 USC 102 as being unpatentable over Tran. This rejection by the Examiner of these claims is respectfully traversed.

It is noted that Tran fails to teach, disclose, suggest or describe diffusion regions having <u>partially</u> overlying polysilicon landings to form N and P type transistors. The Examiner points to FIG. 7 of Tran. Note that this aspect of the embodiment of the above-referenced application is discussed on page 7 and shown in FIG. 1. In Tran, a polysilicon landing does not form N and P transistors, as called for in the rejected claims. Therefore, the rejected claims are not anticipated, as indicated by the Examiner.

The Examiner has also rejected claims 16 and 17 under 35 USC 103 under Tran. This rejection is also traversed. Again, Tran neither teaches, discloses, suggests or describes diffusion regions having partially overlying polysilicon landings to form **N** and **P** type transistors. Likewise, the Examiner has provided no motivation to modify the teachings of Tran.

The Examiner has also rejected claim 20 under 103 on Tran in view of Farwell. However, Farwell fails to cure the deficiencies of Tran. Furthermore, the Examiner has provided no motivation for the combination.

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CONCLUSION

In view of the foregoing, it is respectfully asserted that all of the claims pending in this patent application are in condition for allowance. If the Examiner has any questions, he is invited to contact the undersigned at (503) 264-0967. Reconsideration of this patent application and early allowance of all the claims is respectfully requested.

Respectfully submitted,

Dated:

Gregory D. Caldwell Reg. No. 39,926

c/o Blakely, Sokoloff, Taylor & Zafman, LLP 12400 Wilshire Blvd., Seventh Floor Los Angeles, CA 90025-1026 (503) 264-0967

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